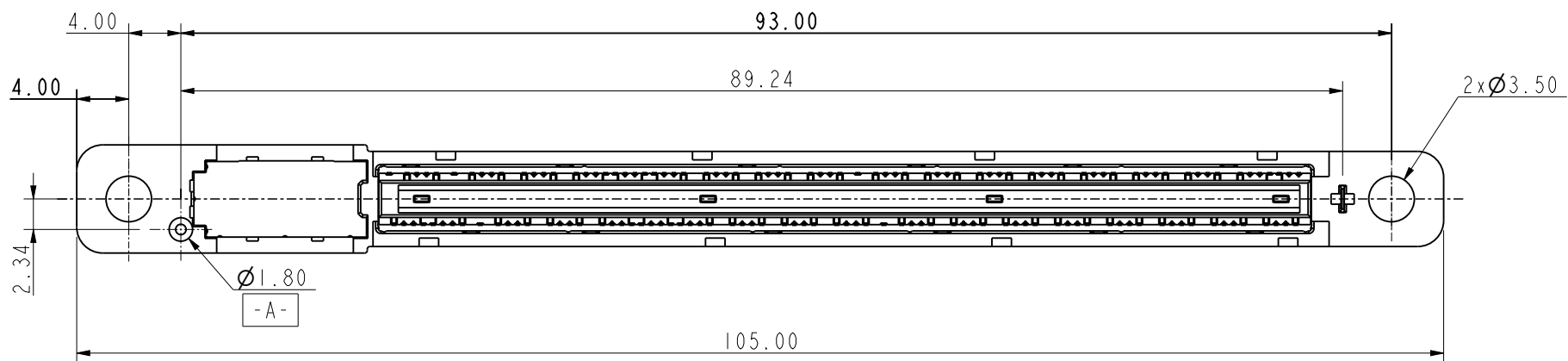
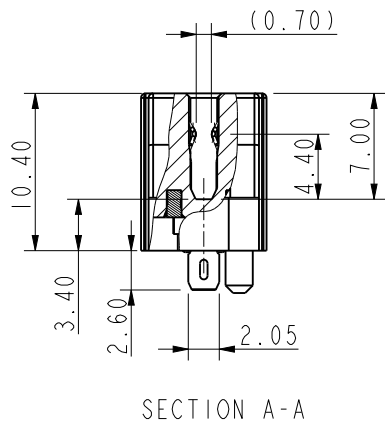
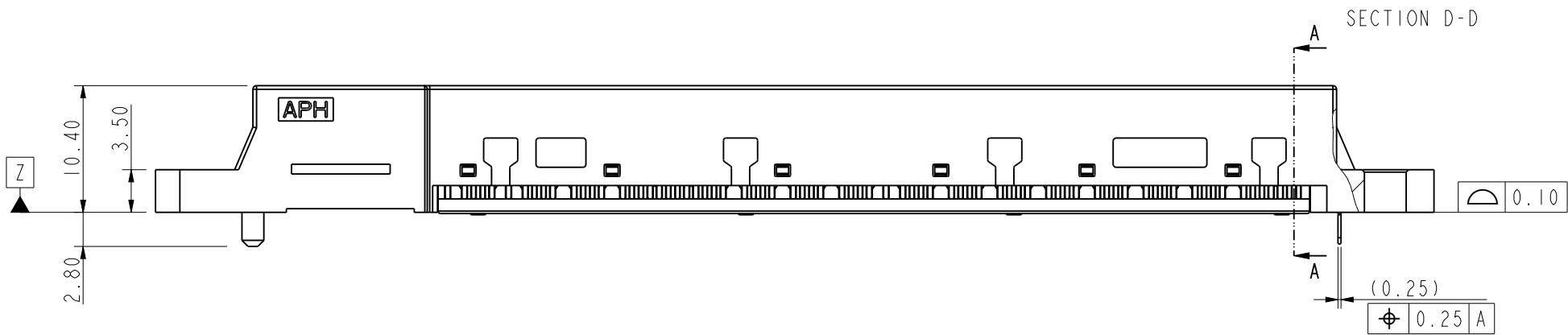
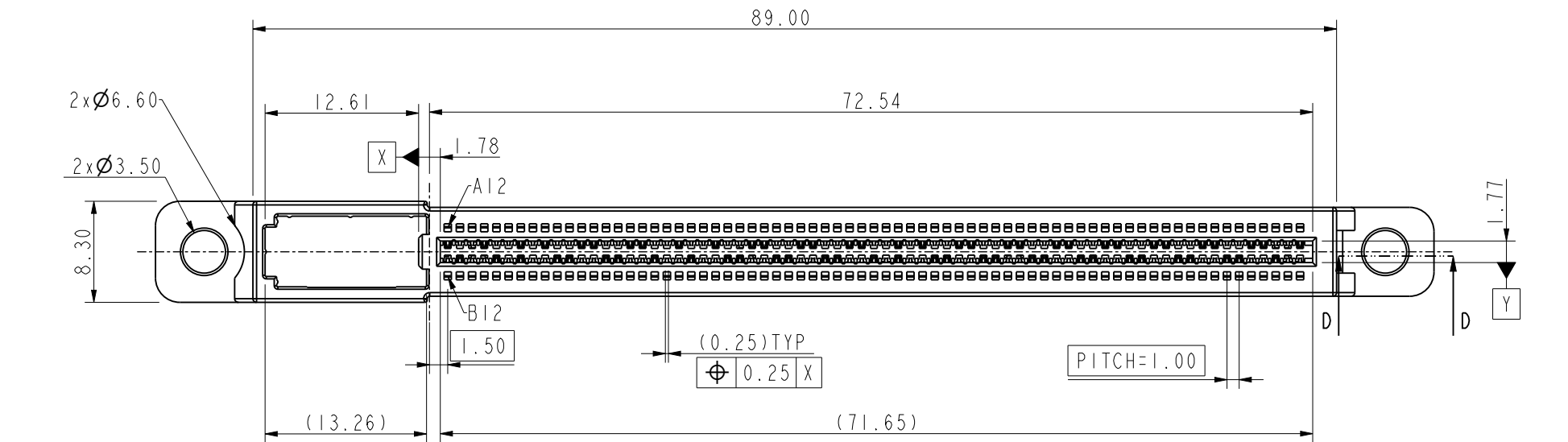
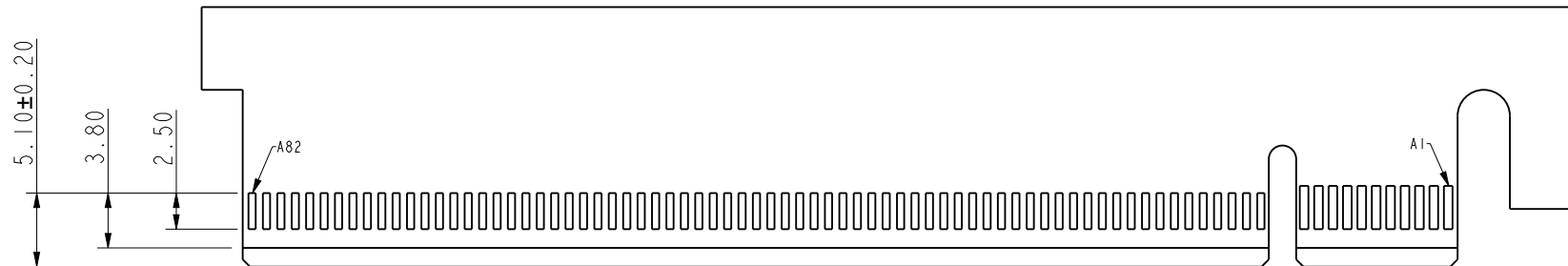
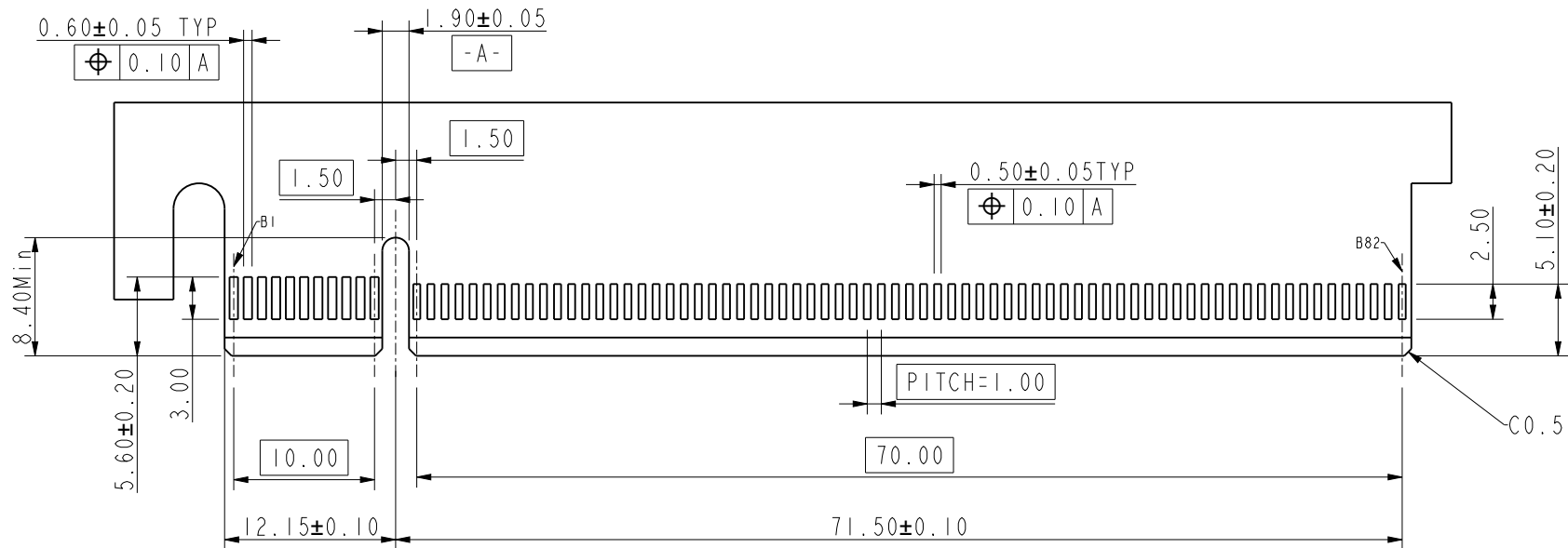


revision history				
rev	ecn no	description	dr	date
XI	-	PROPOSAL	MO	2024/08/16

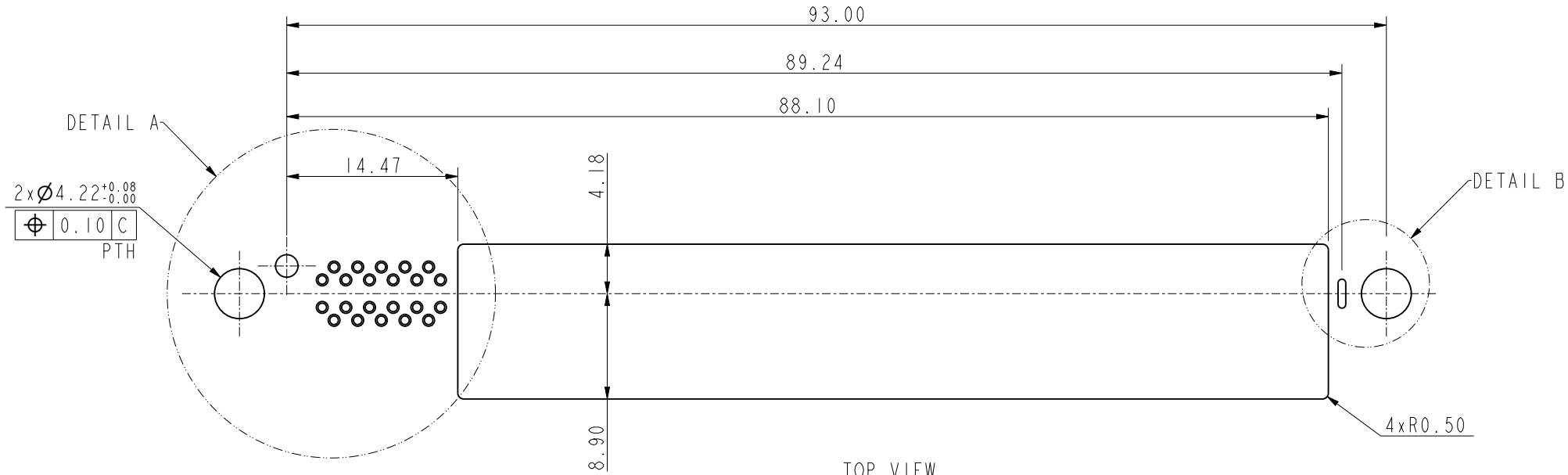


spec ref				dr		Mark.qiu		Aug.16,2024		Amphenol				mm		scale		size					
tolerance std		TOLERANCES UNLESS OTHERWISE SPECIFIED				eng		Mark.qiu						Aug.16,2024		ecn no				1:1		A3	
						rvwr																	
				appr						amphenol-icc.com				rel level				PROPOSAL					
surface		linear		0.X		±0.30		projection		title		PCIE DA CONNECTOR SERIES				dwg no		CEMDA142014022X				rev	
				0.XX		±0.20																	
				0.XXX		±0.05																	
		angular		0°		±2°		product family												sheet 1 of 5			

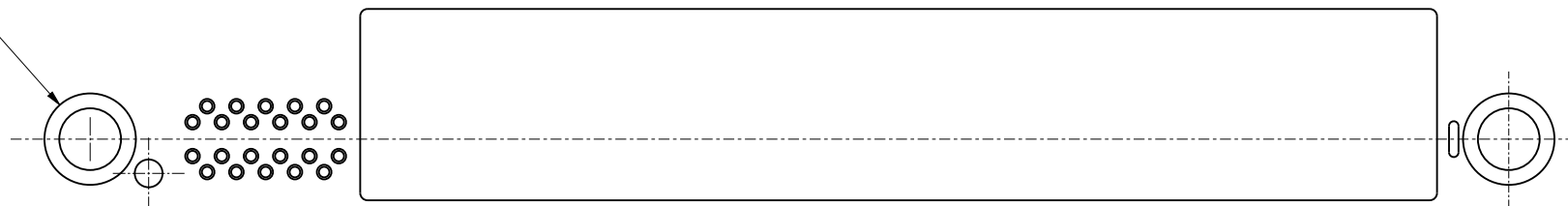


RECOMMEND LAY OUT FOR MATING CARD, THICKNESS=1.57±0.13mm
GENERAL TOLERANCE ±0.05mm

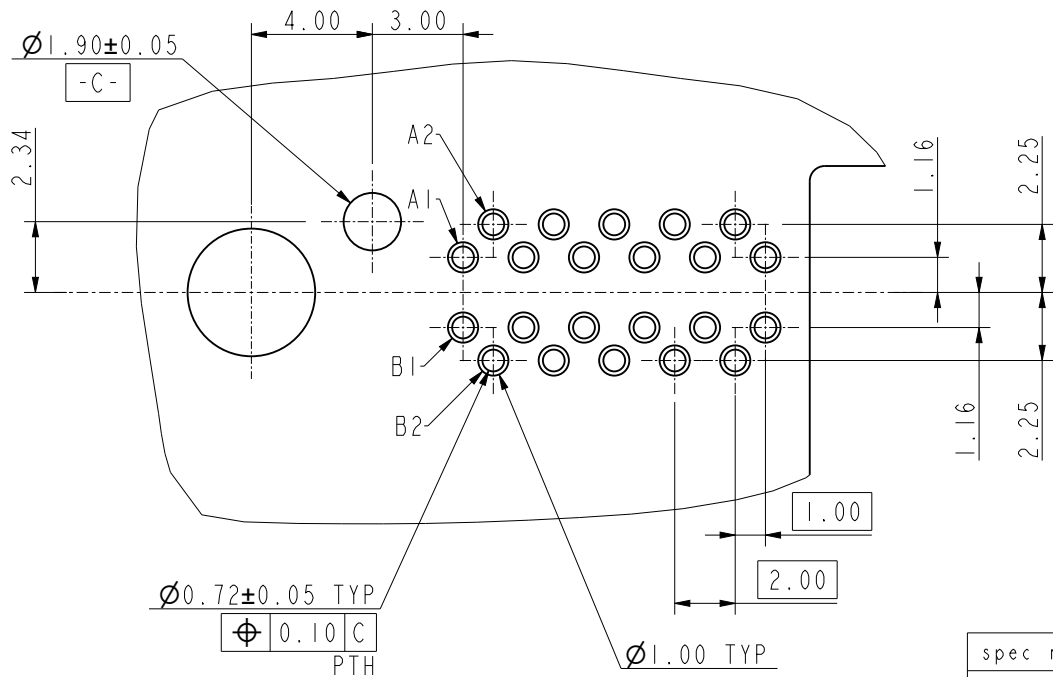
spec ref				dr		Mark.qiu		Aug.16,2024		Amphenol				mm		scale		size					
tolerance std				eng		Mark.qiu		Aug.16,2024						←				1:1				A3	
				rvwr																			
				appr																			
surface				linear		0.X		±0.30		projection		title		PCIE DA CONNECTOR SERIES		diag no		CEMDAI420I4022X				rev	
						0.XX		±0.20						DIRECT ATTACH CABLE X16 WELDING GEN6		cat. no.		X I					
						0.XXX		±0.05															
						angular		0°						±2°									
				✓		angular		0°		±2°				product family		amphenol-icc.com						rel level	
																				sheet 2 of 5			



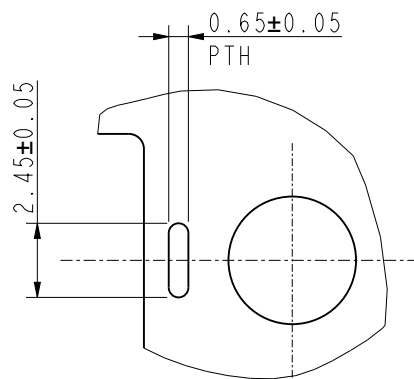
SOLDER PAND 2xØ6.25±0.05
M2.5 SOLDER NUT ON THIS SIDE



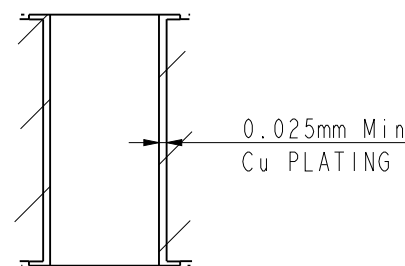
BOTTOM VIEW
RECOMMENDED PCB LAYOUT
GENRAL TOLERANCE ±0.05



DETAIL A
SCALE 4:1



DETAIL B
SCALE 4:1



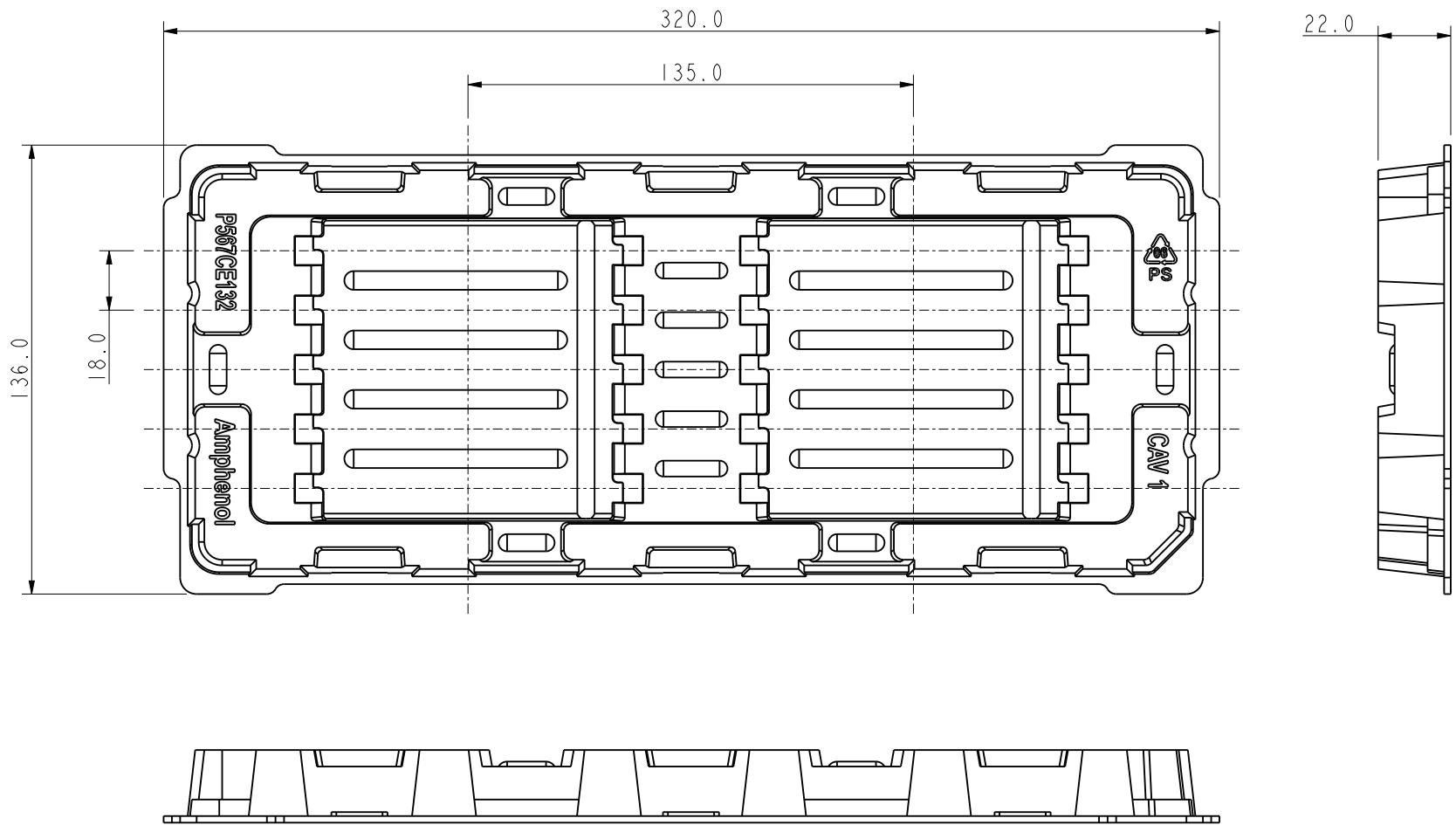
DRILLHOLE RECOMMENDATIONS

spec ref				dr	Mark.qiu	Aug.16,2024	Amphenol		scale	size
tolerance std				eng	Mark.qiu	Aug.16,2024			1:1	A3
				rvwr					ecn no	
				appr					rel level	PROPOSAL
surface				projection			amphenol-icc.com			
				linear	0.X	±0.30	PCIE DA CONNECTOR SERIES		dwg no	CEMDA142014022X
					0.XX	±0.05	DIRECT ATTACH CABLE X16 WELDING GEN6		cat. no.	X1
				angular	0°	±2°	product family		sheet 3 of 5	

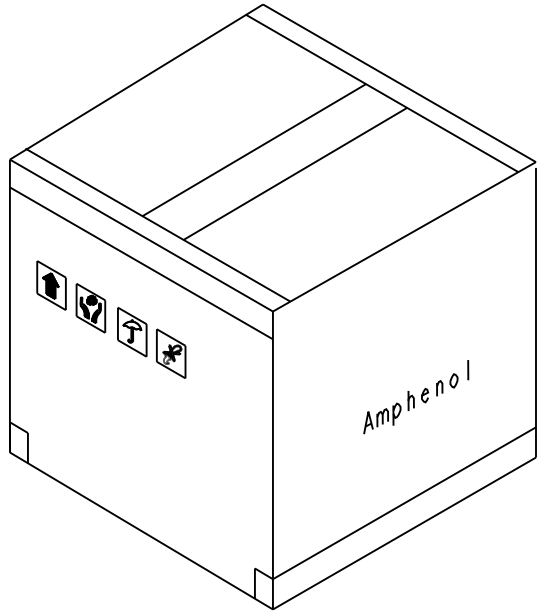
A

C

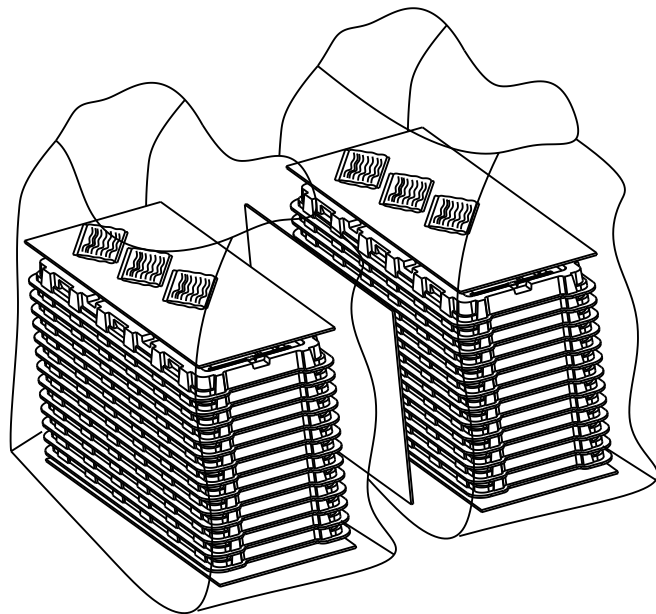
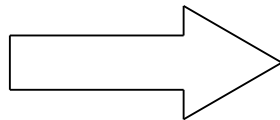
D



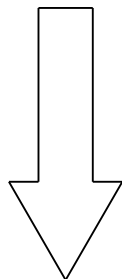
NOTE:
1. TRAY MATERIAL: PS BLACK;
2. PACKING QTR: 10PCS/TRAY
160PCS/CARTON;



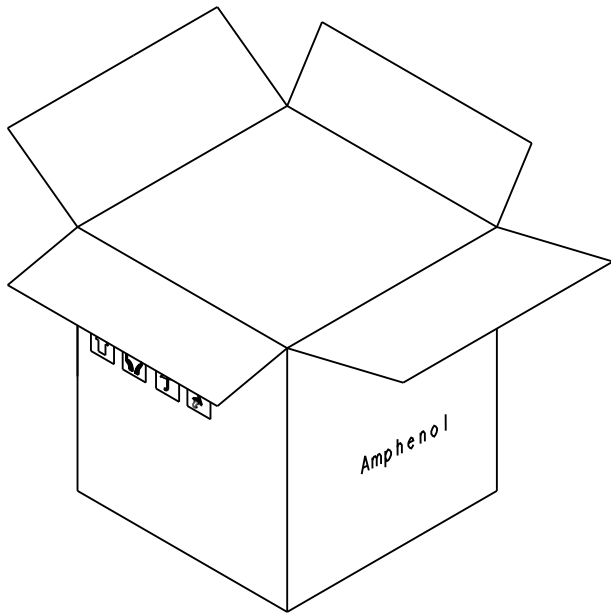
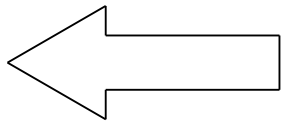
PACKING IN BAG

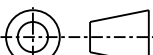


PUT IN CARTON



SEALING CARTON



spec ref				dr		Mark.qiu		Aug.16,2024		Amphenol				mm		scale		size									
tolerance std		TOLERANCES UNLESS OTHERWISE SPECIFIED				eng		Mark.qiu						Aug.16,2024		1:1				A3							
						r vwr										ecn no											
				appr						amphenol-icc.com				rel level				PROPOSAL									
surface		linear		0.X		±3.0		projection 		title		PCIE DA CONNECTOR SERIES						dwg no		CEMDA142014022X						rev	
				0.XX		±2.0																					
				0.XXX		±0.1																					
		angular		0°		±5°																		cat. no.			
product family																				sheet 4 of 5							

A

B

C

D

PRODUCT NUMBER CODE

NOTES:

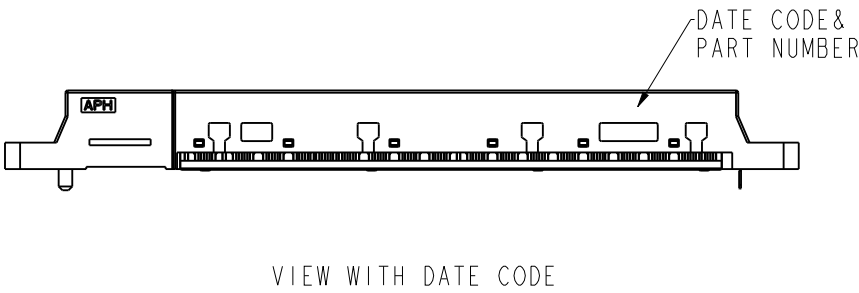
1. MATERIAL:
HOUSING: HIGH PERFORMANCE RESIN, GLASS FILLED, BLACK,UL 94V-0
CONTACT: COPPER ALLOY.
2. PLATING:
CONTACT: UNDERPLATE: 50u" NICKEL OVER ALL.
MATING AREA: SEE PRODUCT NUMBER CODE.
SOLDER AREA: 20u"-80u" TIN(LEAD FREE)
PRESS FIT: 100u" TIN MAX
50u" TIN MIN
3. ELECTRICAL PERFORMANCE(85ohm):
RATED CURRENT FOR SIGNAL PIN:0.7A/PIN
RATED CURRENT FOR POWER PIN:1.1A/PIN(A2/A3/A9/A10/B1/B2/B3/B10 TOYAL 9PIN).
4. MATERIAL SHOULD BE FULFILLED AMPHENOL SPEC # SSN002 AND MEET HALOGEN FREE.
5. DATE CODE "YYMMDDX", YYMMDD IS ASSEMBLY DATE.
6. THE HOUSING WILL WITHSTAND EXPOSURE TO 260°
PEAK TEMPERATURE FOR 10 SECONDS IN A
CONVECTION, INFRA-RED OR VAPOR PHASE
REFLOW OVEN.
7. THERE IS GROUND BUSSING WITHIN THE CONNECTOR SYSTEM.
FOR DIFFERENTIAL PAIR'S GROUND LOCATION, PLEASE REFER TO THE
CONNECTOR PINOUT TABLE FROM
CHAPTER 6 OF PCI EXPRESS CARD
ELECTROMECHANICAL SPECIFICATION.
8. MECHANICAL CHARACTERISTICS
PRESS FIT CONTACT SHALL WITSTAND AN AXIAL RETENTION LOAD OF 300G MINIMUM.
BOARD LOCK SHALL WITHSTAND AN AXIAL RETENTION LOAD OF 1500G MINIMUM.
9. RECOMMENDATION SCREW SIZE: M2.5.

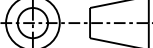
CEMDAI42014022X

NO. OF POSITION
142:142 POSITIONS

PLATING SPEC
1 0.76um Au
2 0.38um Au
3 GOLD FLASH

TRANSMISSION SPEED
2: PCIe Gen6/64Gbps



spec ref				dr		Mark.qiu		Aug.16,2024		Amphenol				mm		scale		size									
tolerance std		TOLERANCES UNLESS OTHERWISE SPECIFIED				eng		Mark.qiu						Aug.16,2024		ecn no				1:1		A3					
						rvwr																					
				appr						amphenol-icc.com				rel level				PROPOSAL									
surface		linear		0.X		±0.30		projection 		title		PCIE DA CONNECTOR SERIES						drg no		CEMDAI42014022X						rev	
				0.XX		±0.20						DIRECT ATTACH CABLE X16 WELDING GEN6								cat. no.						XI	
				0.XXX		±0.05																					
		angular		0°		±2°						product family												sheet 5 of 5			